



"Embedding thoughts into Technology"

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Stand alone PWM Generator chip

Client:

US based company.

Requirement:

To develop a customized solution for providing FPGA Based PWM Controller.

Solution:

The controller was built with Flash Based FPGA from Actel. There were 8 channels of PWM which can be controlled for frequency as well as duty cycle. The Duty cycle can be varied from center and with resolution of 16 bits. The centre frequency can be scaled with ratio of 1:1000 with 3 bit pre-scaling factor. The FPGA runs with clock of 40 MHz. The classical tool chain from ACTEL was used for the development with the modular code developed in Verilog.

Tools/Platforms:

- » FPGA: A3P125
- » JTAG Based Tool Chain from Actel
- » Libero IDE V7.2
- » Verilog